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APPLICANTS

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** CONTINUING DATA *****

This application is a CON of 10/254,600 09/26/2002 PAT 6,730,589
which is a DIV of 09/117,510 04/22/1999 PAT 6,475,896
which is a 371 of PCT/JP97/04437 12/04/1997

** FOREIGN APPLICATIONS *****

JAPAN 8-356880 12/26/1996
JAPAN 9-91449 03/26/1997
JAPAN 8-339045 12/04/1996

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Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 23	TOTAL CLAIMS 1	INDEPENDENT CLAIMS 1
Verified and Acknowledged	Examiner's Signature Initials				

ADDRESS

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TITLE

ELECTRONIC COMPONENT AND SEMICONDUCTOR DEVICE, METHOD OF MAKING THE SAME AND
METHOD OF MOUNTING THE SAME, CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)
<input type="checkbox"/> 1.17 Fees (Processing Ext. of

FILING FEE FEES: Authority has been given in Paper